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Name	Mr Giridhar Padmaraj Jain
Designation	Assistant Professor
Qualification	BE(E&TC) ME(Electronics)
Email	gpjain@witsolapur.org
Experience in years	
Teaching	32
Industry	0
Research	9
Area Of Interest	Electronics Circuit Design, VLSI Design
Subjects Handled	
UG	VLSI Design, Electronics Applications & system Design, Linear Integrated Circuits,
PG	CMOS VLSI Design, Advanced Control Systems
Research Guidance	
PG	14
Ph.D	NA
Publications	
International Journals	17
International	5

Conferences	
National Journals	0
National Conferences	1
Number Of Books Published	1
Number Of Patents	NA
Public Profile URL	https://gpjain.wordpress.com
Professional Memberships	Fellow Institution of Engineers, LMISTE
Achievements & Awards	PCB design support to industries and professionals, Worked as resource person for workshop on Micro controller 89C51 interfacing and PCB fabrication, Coauthor of the book "Electronics Circuit Design" published by Nikita Publications Latur.