S.A.P.D. Jain Pathashala

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An Autonomous Institute

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Personal Information	
Name	Mr. Ravindra Nagnath Chavan
Designation	Assistant Professor
Qualification	ME(Electronics Engineering)
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Experience in Years	
Teaching	25 Years
Industry	4 Years
Research	
Area of Interest	VLSI Design, Embedded Systems
Subjects Taught	
UG	Microcontrollers, Embedded Systems, CMOS VLSI Design
PG	
Research Guidance	
PG	
Ph.D.	
Publications	
International Journals	
International Conference	4
Number of Books Published	2
Number of Patents	
Public Profile URL	https://rnchavansite.wordpress.com/
Professional Memberships	ISTE, IE
Notable Achievements and	
Awards	