



S.A.P.D. Jain Pathashala

# Walchand Institute of Technology, Solapur

An Autonomous Institute

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## Electronics and Computer Engineering Lab Details

Lab Name	Applied Electronics Laboratory
Lab Description	To analyze and design electronic circuits using active and passive components.
Lab Equipment	Signal Generator, Power supply, Digital Storage Oscilloscope, Dual Trace CRO 25MHz, LCR Q meter
Lab Floor Area in square meters	85.12
Practical's Engaged in Lab	Electronics Circuit Analysis & Design (S. Y. B. Tech. Electronics Sem I), Network Theory and Analysis (S. Y. B. Tech. Electronics Sem - II)
Name of Lab Incharge	Mr. S. C. Kalshetti
Associated Faculty with the lab	Mr. S. C. Kalshetti, Mr. A. S. Gundale
Name of Lab Assistant	Mrs. A. B. Jadhav
Name/s of Menial Staff	Mr. H. P. Annadate