S.A.P.D. Jain Pathashala



Walchand Institute of Technology, Solapur

An Autonomous Institute

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Electronics and Telecommunication Engineering Lab Details

| Lab Name | Basic Electronics |
|---------------------------------|--|
| Lab Description | Basic Electronics Lab is used to provide engineering skills by the way |
| | of breadboard circuit design with electronics design and components |
| | to students. Also to design and analyze various electronic circuits such |
| | as applications of operational amplifier, RC coupled amplifier, |
| | oscillators etc. |
| Lab Equipment | Digital Storage Oscilloscope, Regulated Dual DC Power Supply, |
| | Signal Generator, Auto Compute LCR Meter, Multi meter, Bread |
| | Boards & Consumable Items |
| Lab Floor Area in square meters | 94.66 Sq.Mtr |
| Practical's Engaged in Lab | Analog Integrated Circuits, Electronic Circuit Analysis & Design, |
| | Network Theory & Analysis |
| Name of Lab Incharge | Mr. K. R. Biradar |
| Associated Faculty with the lab | Mr. K. R. Biradar, Mrs. P. P. Gambhire |
| Name of Lab Assistant | Mrs. A.S. Kassa |
| Name/s of Menial Staff | Mr. Bansode. S. J |