



S.A.P.D. Jain Pathashala

Walchand Institute of Technology, Solapur

An Autonomous Institute

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Electronics and Telecommunication Engineering Lab Details

Lab Name	Control System
Lab Description	Control System lab is used specially to study the different characteristics of Potentiometer as error detector, Liquid Level Controller, synchro units, etc. Here practical of the subject Control System, Network Theory & Analysis, Linear Integrated Circuits, etc. are conducted. Also students investigate and analyze their project in this laboratory.
Lab Equipment	Cathode ray Oscilloscope, Variable Dual DC power supply, Digital Storage Oscilloscope, Function Generator, Multimeter
Lab Floor Area in square meters	94.66 Sq.Meter.
Practical's Engaged in Lab	Network Theory Analysis
Name of Lab Incharge	Mr.S.V. Aursang
Associated Faculty with the lab	Mr.S.V. Aursang, Mr. Biradar K.R.,Mr.S.P.Swami
Name of Lab Assistant	Mrs. A. S. Kassa
Name/s of Menial Staff	Mr. Bansode S.J.