



S.A.P.D. Jain Pathashala

# Walchand Institute of Technology, Solapur

An Autonomous Institute

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## Electronics and Telecommunication Engineering Lab Details

Lab Name	Project
Lab Description	The Project Laboratory offers the students the opportunity to gain valuable hands-on experience in a state-of-the-art environment where students become proficient in both the physical and creative skills needed in the field of Electronics and Telecommunication Engineering. The heterogeneous projects hardware & software are implemented in the project lab. The laboratory is utilized for research and development activities by students and faculty. The Project Laboratory has a key role in promoting practical learning experience where students develop creative proposals, and execute their final projects. Students work in project laboratories and participate in various design & development of hardware and/or software competitions.
Lab Equipment	CRO, DSO, Dual P/S, DMM, Microcontroller Programmer.
Lab Floor Area in square meters	71.23 Sq.Meter.
Practical's Engaged in Lab	Final Year BTech. Project, TY B.Tech mini projects.
Name of Lab Incharge	Dr. Mrs. Mergu R. R.
Associated Faculty with the lab	Dr. Ms. A.V. Thalange, Dr. R. R. Dube, Dr. Mrs. R. R. Mergu, Mr. S.R. Barbade, Mr. V.H. Kondekar, Mr. S.V. Aursang, Mr. P.Y. Kumbhar, Mr. P.D.R. Patnaik, Mr. S.C. Chamatgoudar, Mr. K.R. Biradar, Mr. R.R. Konapure, Mrs. P.S. Shetgar, Mrs. P.S. Udage, Mrs. G.S. Chavan
Name of Lab Assistant	Mr. S.V. Warekar
Name/s of Menial Staff	Mr. S.S. Vhankade