S.A.P.D. Jain Pathashala Walchand Institute of Technology, Solapur



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Electronics and Computer Engineering Lab Details

Lab Name	Analog And Digital Electronics Engineering
Lab Description	To design and analyze different analog electronic circuits
	like oscillators filters.
	To design and analyze digital logic circuits like
	combinational and sequential.
Lab Equipment	Signal Generator, CRO(30 MHz Two channel four trace),
	Power Supplies, Instrumentation Tutor, Digital Trainer Kits,
	DSOs-150MHz
Lab Floor Area in square	66
meters	
Practicals Engaged in Lab	Digital Logic Design (S.Y.B.Tech-Electronics,Sem-I)
	Electronic System Design (B.TechElectronics,Sem-I)
	Analog Integrated Circuits (S.Y.B.Tech-Electronics, Sem-II)
Name of Lab Incharge	Mr.S.A.Utage
Associated Faculty with the	Mr. D. N. Chavan, Mr. S. A. Litaga, Ma D. D. Datil
lab	Mr.R.N.Chavan, Mr.S.A.Utage, Ms.P.B.Patil
Name of Lab Assistant	Mrs.V.M.Kulkarni
Name/s of Menial Staff	Mr.H.P.Annadate