

## S.A.P.D. Jain Pathashala Walchand Institute of Technology, Solapur An Autonomous Institute



Personal Information	
Name	Dr Rajendra R Dube
Designation	Professor
Qualification	Ph,D (ENTC)
E-Mail ID	rrdube@witsolapur.org
Experience in Years	
Teaching	36
Industry	0
Research	15
Area of Interest	Embedded System, Signal Processing, Communication System
Subjects Taught	
UG	Digital Signal Processing, Embedded System
PG	ADSP, RM & IPR ,Sensors & Actuator
Research Guidance	
PG	24
Ph.D.	03
Publications	
International Journals	55
International Conference	40
Number of Books Published	
Number of Patents	
Public Profile URL	
Professional Memberships	ISTE /IE
Notable Achievements and Awards	<ol> <li>Grant received under RPS from AICTE for research work</li> <li>Award for Contribution towards Research and Publications, WIT Solapur</li> <li>Award for Academic Excellence, WIT Solapur</li> <li>Best award received for Contribution towards Organization in Teacher</li> <li>Category from Shriman Bhausaheb Gandhi Pratishthan Solapur</li> <li>Runner-up in Engineering &amp; Technology Category at Ph.D(Teacher)</li> </ol>

Level of "Avishkar 2014" University Level Research Festival
6. Grant Received from Solapur University, for IOT, Embedd
System, Antenna Fabrication & Design for conducting
workshop since last Three Years.
7. Working as BOS Chairman (Electronics &
Telecommunication Engineering) for Solapur University,
Solapur
8. Working as BOS Member (Electronics Engineering) for
Solapur University, Solapur
9. Editorial board member of AR Research Publication and
Conference World
10. Editorial board member of IJCRT RMS International
Journal of Creative Research Thoughts IJCRT (www.ijcrt.org
(ISSN: 2320-2882)
11. Editorial board member of Journal of Electronics &
Communication Systems (MAT Journal)
12. Won the ISTE II prize for the project titled VLSI Based
system for predicting Ventricular Arrythmia
13. Worked as Reviewer at International Conference on
Communication, Information and Computing Technology,
2021, IEEE Bombay Section in IOT, VLSI and Embedded
Systems.
14. Working as Technical Committee member at
ICISPC, Japan, 2021.
15. Received Life Time achievement award from Indian
Science and Technology Chennai on 14 <sup>th</sup> May 2022.