



S.A.P.D. Jain Pathashala

Walchand Institute of Technology, Solapur

An Autonomous Institute

P.B.No.634, WalchandHirachand Marg,Ashok Chowk, Solapur - 413006 (Maharashtra)

Phone: (0217)2652700, 2391700**Extn:** 318| **Fax:**(0217)2651538

Email: principal@witsolapur.org | principal.witsolapur@gmail.com



Personal Information	
Name	Akshata Deepak Wadgaonkar
Designation	Assistant Professor
Qualification	M.Tech
E-Mail ID	akshatawadgaonkar1998@gmail.com
Experience in Years	
Teaching	1.6 Year
Industry	-
Research	-
Area of Interest	Image Processing, VLSI, CMOS.
Subjects Taught	
UG	Analog Integrated Circuit, VLSI, Electronic Circuit Design, CMOS, Image and Video Processing
PG	-
Research Guidance	
PG	-
Ph.D.	-
Publications	
International Journals	6
International Conference	1
Number of Books Published	-
Number of Patents	-
Public Profile URL	https://www.linkedin.com/in/akshata-wadgaonkar-1a2110212
Professional Memberships	-

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<p>Notable Achievements and Awards</p>	<ul style="list-style-type: none">• Scored in Swayam NPTEL Elite + Silver for the subject Accreditation and Outcome Based Learning.• Participated and completed a 2 days Phase 1 of Creators of Metaverse FDP workshop organised by AICTE and supported by Meta Platform Inc.• Participated in the NEP 2020 Orientation & Sensitization Programme under the Malaviya Mission Teacher Training Programme (MM-TTP) of the University Grants Commission (UGC) Organised by UGC-Malaviya Mission Teacher Training Centre, University of Mumbai (Maharashtra) from 15th January 2024 to 24th January, 2024.• Completed a 5 days FDP Programme on Cloud Infrastructure (AWS) organised by E&TC Department of SINHGAD ACADEMY OF ENGINEERING, Pune Maharashtra in collaboration AICTE with Brain vision Solutions India Pvt. Ltd.• Completed a 5 days FDP Programme on VLSI to System Design Silicon to End Application Approach, organised by AICTE, ARM Education and STMicroelectronics.
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