Personal Information	
Name	Dr Ms. Asha Vijaykumar Thalange
Designation	HOD, E&TC Engg. and Associate Professor
Qualification	BE (Electronics Engg.) ME (E&TC-VLSI and Embedded Systems) Ph.D.(E&TC)
E-Mail ID	avthalange@witsolapur.org
Experience in Years	
Teaching	21
Industry	nil
Research	15
Area of Interest	Signal and Image Processing, VLSI Technology, Data Analytics and Machine Learning
Subjects Taught	
UG	Analog Communication, Principles of Digital Communication, Data Analytics, Digital Techniques, Signals and System, VLSI Design, Electronics System Design
PG	Nil
Research Guidance	
PG	02 (Pursuing)
Ph.D.	02 (Pursuing)
Publications	
International Journals	19
International Conference	25
Number of Books Published	--
Number of Patents	02
Public Profile URL	https://www.linkedin.com/in/dr-asha-thalange-70836b17
Professional Memberships	Associate Member of IE(I), ISTE Member

Notable Achievements and Awards	- Received a **Grant of Rs. 2, 97, 000/- from AICTE** under the scheme of **Career Award for Young Teacher (CAYT)** for 3 years from 2013-14 to 2015-16 for a Research project titled "Vision Based Recognition Of Alphabets In American Sign Language (ASL) Using Hybrid Methodology Of Feature Extraction Techniques". - Granted **Two Australian Innovation Patent** - Secured **Second Prize at West Zone "Anveshan 2015: Students Research Convention**", in the area of Social Science and Humanities organized by, Association of Indian Universities, Delhi and Hosted by Maharaja Ganga Singh University, Bikaner, Rajasthan on 12th and 13th January, 2016. - Represented University at National Level Anveshan Competition held at Sardar Patel University, Anand, Gujarat on 28th to 30th March, 2016 - Secured **First Prize at "Avishkar 2015: University Level Research Festival**", in the area of Humanities, Languages, Fine Arts Category at M.Phil./ Ph. D. Level organized by Solapur University, Solapur and Hosted by Shriram Gramin Sanshodhan Va Vikas Pratishthan's Biotechnology College, Wadala, Tal. N. Solapur, Dist. Solapur on 30th and 31st December, 2015. - Received **SAP Award of excellence** and is **among top 253 participants** is top performers in FDP **"Use of ICT in Education for Online and Blended Learning"**, conducted by IIT, Mumbai. **Top Performer** in FDP on" **Mentoring Educators in Educational Technology (FDP 301x)** - Awarded a Certificate of Appreciation as a **Top Performing Mentor** for the **NPTEL Online Certification course** Introduction to Operating Systems by NPTEL. - Secured a **Best paper Presentation Award** for presenting a paper titled "*Static Hand Gesture Recognition of American Sign Language Alphabets*" at **ETCC-15, 2nd International Conference on Emergent Trends in Computing and Communication,** In Collaboration with IJCA (International Journal of Computer Applications), organized by Centurion University of Management and Technology, Bhubaneshwar, Odisha, India on 2nd May, 2015. - Received a "**Best Participant Award**" in **ISTE Approved STTP** (Short Term Training Program) on "**Image processing and Machine Learning**", Organized by E&TC Department, N. B. Navale Sinhgad College of Engineering,

	Kegaon, Solapur, Maharashtra, during 22nd to 26th December, 2014.
	• Received **Certificate of Appreciation** as a **Top Performing Mentor** four times for courses Introduction to Internet of Things, Introduction to Operating Systems, Developing Soft Skill and personality.
	• Won **Best Paper of the Session Award** at **National Conference on Signal Processing "NCSP'09"** for presenting the paper titled "Classification of arrhythmias using spectral analysis", organized by MIT, Aurangabad on March 2009.