



S.A.P.D. Jain Pathashala

Walchand Institute of Technology, Solapur

An Autonomous Institute

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Electronics and Computer Engineering Lab Details

Lab Name	Computer Lab-I
Lab Description	Laboratory have infrastructural facilities equipped with Core I5 computers and electronics system development board like ACTEL, Xilinx, PSoC etc. The laboratory is used for Object Oriented Programming, modern digital design synthesis & analysis, and programming.
Lab Equipment	Lenovo M70 Tower SSF Core i5 9th Gen, Lenovo M70 Tower SSF Core i5 10th Gen, HCL Infinity A380 Core i5 Intel H61 Chipset, Laptops – DELL Intel Core 2 Duo 2.2 GHz, HP Core I3 350 2.26 GHz, HP Laserjet 1020 Plus Printer, Canon Lite110 Scanners, Xilinx Spartan 3 Development Boards, ACTEL FPGA Boards, PSoC 3, PSoC 5, PSoC 2 Development Boards, Advanced VLSI Trainer kit
Lab Floor Area in square meters	85.12
Practicals Engaged in Lab	Object Oriented Programming with C++, Machine Learning, Database Management System, VLSI Design, Final year Projects
Name of Lab Incharge	Mr. R. N. Chavan
Associated Faculty with the lab	Mr. P. S. Malge, Mrs. M. N. Shrigandhi, Mr. K. V. Hulle
Name of Lab Assistant	Mr. N. B. Res
Name/s of Menial Staff	Mr. P. S. Samarth