

S.A.P.D. Jain Pathashala



Walchand Institute of Technology, Solapur

An Autonomous Institute

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Electronics and Telecommunication Engineering Lab Details

Laboratory Name	Computer
Laboratory Description	This laboratory is equipped with major computing and development facilities to support practical learning and advanced experimentation. It includes Core i5 systems with 4 GB RAM and 500 GB HDD, Advanced VLSI Pro ASICs 3 LS Starter Kits with Libero IDE v8.1, Advanced VLSI M-Fusion University Evaluation Kits with Libero IDE v8.3, Advanced VLSI Trainer Kits from U-Square Technology, and licensed MATLAB software. In addition to regular laboratory work, students are encouraged to enhance their programming and problem-solving skills for coding competitions and hackathons using online platforms such as HackerRank and HackerEarth. To support these activities, the laboratory is provided with a high-speed internet connection available throughout working hours. The lab is also utilized for Training and Placement (T&P) activities, including conducting online aptitude and technical tests. Practical sessions in the laboratory involve programming in Java and Python, enabling students to gain hands-on experience and industry-relevant skills.
Lab Equipment	Major Equipments in the Lab- <ul style="list-style-type: none">● Core i5 with 4GB RAM, 500GB HDD.● Advanced VLSI Pro Asics 3 LS Starter Kits with Libero IDE V8.1● Advanced VLSI M Fusion University Evaluation kits with Libero IDE V 8.3● Advanced VLSI Trainer KIT, U-square Technology.● MATLAB
Lab Floor Area in sq.m.	86.64 Sq. mtr.
Practical's Engaged in Lab	Multidisciplinary Minor-II, Multidisciplinary Minor-IV, C Programming, Python Programming, Data Structures, CMOS VLSI Design, Project.
Name of Lab Incharge	Dr. R. R. Konapure
Associated Faculty with the lab	Mrs.P.H. Shetgar, Mr. S. P. Swami, Mr. S. N. Chamatagoudar, Mr. S. P. Kulkarni, Ms. P.P. Gambhire, Mr. A. B. Dhulkhekar
Name of Lab Assistant	Mr. Om Navale
Name/s of Menial Staff	Mr. A. S. Mheta